

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	6690005
<b>Application Number:</b>	10719020
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9168
<b>Title of Invention:</b>	Method for preparing a circuit board material having a conductive base and a resistance layer
<b>First Named Inventor/Applicant Name:</b>	Akira Matsuda
<b>Customer Number:</b>	38834
<b>Filer:</b>	Shuji Yoshizaki
<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	032130
<b>Receipt Date:</b>	22-DEC-2009
<b>Filing Date:</b>	24-NOV-2003
<b>Time Stamp:</b>	15:49:19
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part /.zip	Pages (if appl.)
1	Transmittal Letter	032130_IDS_Transmittal_12_2 2_2009.pdf	54378 9916d7d742610f5cd160c6a1261057e5a65 bce06	no	2

### Warnings:

### Information:

2	Information Disclosure Statement (IDS) Filed (SB/08)	032130_IDS_SB_08a_12_22_20 09.pdf	785661 1c16ea0657c7c74fb678190d86d14c4033ff be7c	no	4
<b>Warnings:</b>					
<b>Information:</b>					
3	Foreign Reference	CN1037039.pdf	458120 992d7c33d66e72f2a190a0987937861784d bb037	no	10
<b>Warnings:</b>					
<b>Information:</b>					
4	Foreign Reference	TW419530.pdf	1062682 2fab91242cd0bade7015db6b5b1f9b10a24 e22e9	no	25
<b>Warnings:</b>					
<b>Information:</b>					
5	NPL Documents	TaiwaneseOA.pdf	831072 54a53be52d2ef44046232f9cc8a2cf4c4229 6760	no	6
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>				3191913	
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